TABLE 1 Comparison Between Proposed Coupler and Previous Works

	[4]	[5]	[6]	This work
Technology	Branch line	Coupled line	Patch	Patch
Circuit size	$0.25\lambda*~0.25\lambda$	$0.25\lambda * 0.25\lambda$	$0.5\lambda*~0.5\lambda$	$0.5\lambda*~0.5\lambda$
Realizable coupling coefficient (dB)	10	20	10	15
High-frequency applications	No	No	Yes	Yes
Narrow gap	Yes	Yes	No	No
BW (%) (10 dB)	33.3	30	5	29
0 1		100	110	

$$Y_{oe} = \frac{1}{X_{LH1} + \frac{2X_{CH1}X_{LH3}}{2X_{CH1} + X_{LH3}}} + \frac{1}{X_{LV1}},$$
 (9d)

where

$$X_{CH1,CV1} = \frac{1}{j\omega C_{CH1,CV1}},$$

 $X_{LH1,LH3,LH4,LV1,LV3,LV4} = j\omega L_{LH1,LH3,LH4,LV1,LV3,LV4}$ .

It can be found that the related parameters are similar to those for the patch coupler with centrally loaded shorting post. But the two parameters  $L_{\rm LH3}$  and  $L_{\rm LV3}$  for this configuration can be controlled by the positions of shorting posts ( $P_1$  and  $P_2$ ). Therefore, the range of realizable N can be extended by choosing suitable  $P_1$  and  $P_2$  maintaining the good input/output matching and quadrature phase characteristics.

According to the analysis described in the last section, a 15 dB patch quadrature coupler was designed and fabricated using Rogers RT/Duroid 5870 substrate with dielectric constant ratio 2.33 and thickness h = 0.787 mm. The resulting dimensions of the circuit are:  $R_1 = 7$  mm,  $R_2 = 9$  mm,  $R_v = 0.5$  mm,  $P_1 = 3$  mm, and  $P_2 = 2$  mm. Figure 10 shows the photograph of the designed 15 dB patch quadrature coupler.

Figure 11 shows the simulated and measured frequency responses in terms of S-parameters for the designed 15 dB quadrature patch coupler. Good agreement between simulation and measurement can be observed. The coupling coefficient |S31| is 15  $\pm$  1 dB across the 8–9.7 GHz band. The measured phase difference between the two output ports is  $90^{\circ}\pm5^{\circ}$  within the frequency band from 8 to 9.7 GHz. Therefore, the range for the realizable coupling coefficient is extended to 15 dB using the four shorting posts, which cannot be realized using the conventional configuration.

Table 1 summarizes the comparison between the proposed patch quadrature coupler and other structures in the literature. The proposed configuration is found to exhibit the highest fabrication simplicity among the existing configurations for quadrature coupler [1–5], as it eliminates the narrow microstrip line and coupling gap. In addition, the performance of microstrip line based circuits [4,5] becomes poorer with increasing operating frequency; the proposed patch based structure is more suitable for high-frequency applications. Finally, for the first time, the coupling coefficient of patch based quadrature coupler is extended to 15 dB.

#### 4. CONCLUSION

In this article, a method to extend the coupling coefficient range and bandwidth of a patch quadrature coupler is proposed with theoretical analysis for the first time. For demonstration, two patch quadrature couplers have been designed to provide the loose couplings of 10 and 15 dB with wide bandwidths. The

validity of proposed approaches had been demonstrated by simulation and measurement.

#### **ACKNOWLEDGMENT**

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#### STRAIN AND CURVATURE-INDEPENDENT TEMPERATURE SENSOR BASED ON AN INTERFEROMETER TAPER FABRICATED WITH A CO<sub>2</sub> LASER

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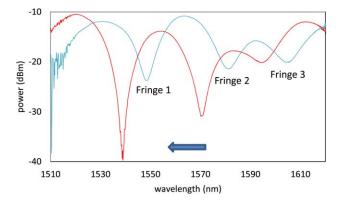
ABSTRACT: An optical fiber interferometer taper fabricated with a CO₂ laser is proposed for strain and curvature-independent temperature measurement. Variations in temperature produce changes in the conditions of the interference between light traveling along the core and cladding and a linear behavior is verified for the relation between the wavelength of the resonant loss peak and temperature, yielding a sensitivity of 110 pm/°C for a range between 25 and 510°C. Both the applied strain and curvature only promote significant changes in the transmitted power, leaving the wavelength of the resonant loss peak approximately constant and rendering this optical sensing device a good strain and curvature-independent temperature sensor. © 2016 Wiley Periodicals, Inc. Microwave Opt Technol Lett 58:688–691, 2016; View this article online at wileyonlinelibrary.com. DOI 10.1002/mop.29640

Key words: optical fiber sensor; taper; interferometer

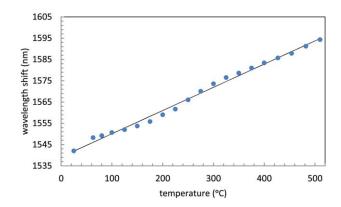
#### 1. INTRODUCTION

The simultaneous variation of external parameters, such as temperature, curvature, and applied strain, which unavoidably takes place outside a controlled environment, has driven much of the work developed in optical fiber sensing. Several devices have been proposed to overcome this hindrance and more exactly determine the impact of the variation of a single external parameter in the characteristics of the light that interacts with said devices.

Tapers can be used as sensors [1-4]. Two bulge tapers are fabricated in series by a fusion splicer in order to create a Mach Zenhder interferometer [1]. The sensitivities obtained for curvature measurement are  $-31.821 \text{ nm/m}^{-1}$  and  $-17.051 \text{ nm/m}^{-1}$ for small and large range of curvature while the sensitivities for temperature are quite small, being the largest 0.0042 nm/°C, which results in a temperature-independent curvature sensor. A taper was fabricated in a large-mode-area fiber (MOF) with a silica core surrounded by air holes arranged in a hexagonal pattern [2] with a temperature sensitivity of 0.0012 nm/°C. Tapers are fabricated using a butane flame burner to heat the fiber [3]; the largest temperature sensitivity is 0.010 nm/°C and the sensitivity to strain is 0.095 nm/ $\mu\epsilon$ , which makes this sensing device an almost temperature-independent strain sensor. A photonic crystal fiber PCF tapered fiber, spliced at both ends to a singlemode fiber (SMF), is produced to obtain an intensity temperature-



**Figure 1** Transmission spectrum evolution for decreasing temperature. The arrow sets the trend for the evolution of the resonant loss peak wavelength for decreasing temperature. [Color figure can be viewed in the online issue, which is available at wileyonlinelibrary.com]



**Figure 2** Temperature response of the interferometer taper using the second wavelength peak. Line (linear fit). [Color figure can be viewed in the online issue, which is available at wileyonlinelibrary.com]

independent curvature sensor [4], with high sensitivity of  $8.35 \text{ dBm/m}^{-1}$  in the following range  $0.87-1.34 \text{ m}^{-1}$ .

 $CO_2$  lasers have also been used to fabricate long-period grating (LPG) structures [5,6]. The reported sensitivities are -7.6 pm/ $\mu\epsilon$  and 3.91 pm/°C, for a LPG inscribed in a PCF [5], which makes the device good for strain sensing since the temperature sensitivity is rather small. A larger temperature sensitivity is also reported and equal to -0.130 nm/°C when the LPG is inscribed in a standard SMF28 fused silica optical fiber [6].

The work presented aims at evaluating the sensitivities of an optical fiber interferometer taper fabricated with a  $CO_2$  laser to temperature, strain, and curvature. The proposed device is interrogated in transmission and the evolution of the fringes of the analyzed spectrum registered for different values of the external parameters. Considering the evolution of the wavelength of the resonant loss peaks  $\lambda_m$  m = 1,2,3, a good temperature sensor is obtained, with very small sensitivity to strain and curvature.

#### 2. FABRICATION AND EXPERIMENTAL RESULTS

Taper fabrication is accomplished using an experimental setup including a CO2 laser and a set of PC controlled motorized stages. The CO<sub>2</sub> laser active medium is excited by an electrical signal with pulse width modulation (PWM) of 25%, corresponding to a laser power of 4.5 W, and a frequency of 10 kHz. The stages parameter values are set so the y-stage and the x-stage have speeds equal to 14  $\mu$ m/s and 8  $\mu$ m/s, respectively. The taper length is set equal to 10 mm. The duration of the fabrication process is 1 min and 42 sec. In order to characterize the tapers fabricated using the CO<sub>2</sub> laser as sensors of curvature and strain, the fiber is fixed onto a manually controlled translation stage as well as onto a block placed at a distance 2L from the stage. As the distance between the two points where the fiber is fixed changes, so does the curvature of the portion of the fiber between the stage and the block. If this portion of the fiber contains the taper, then it will also experience a change in curvature. The characterization in strain is performed moving the stage up to the point where there is no curvature, and then beyond that point in order to stretch the fiber and increase the applied mechanical stress. The resulting changes in the spectrum of the transmitted light are evaluated using an Optical Spectrum Analyzer (OSA). The broadband light source has a central wavelength of 1,550 nm, with a bandwidth of 100 nm and an average power of -30 dBm (Figure 1).

Light propagating along the core and the cladding has different refractive indices associated. This difference in refractive index  $\Delta n$  can be determined using the wavelength spacing  $\Delta \lambda$  between two loss peaks in the transmitted spectrum; in this

case, resorting to the spectrum,  $\Delta n \sim 8 \times 10^{-3}$ , which means the fabrication process has left the core still with an appreciable diameter and a significant portion of the light still travels along the core even at the tapered region. In this case, the taper presents a similar behavior to a Mach Zenhder configuration.

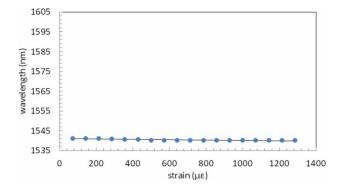
The experimental setup designed to perform temperature characterization is similar to the one implemented for strain and curvature characterization except for the inclusion of an oven, responsible for controlling the taper temperature. The manually controlled stage was kept in the setup in order to allow for a quick change of the taper curvature making it easier to tune the characteristics of the transmitted spectrum.

#### 2.1. Temperature Measurements

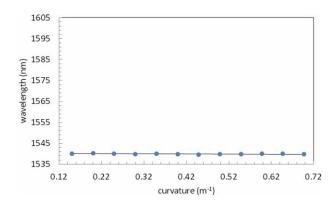
The attention was focused on evaluating how the wavelength of the resonant loss peak  $\lambda_m$  and power of the minima  $P_m$  evolve as temperature changes. For the tapers under study, there is a simultaneous variation in both  $\lambda_m$  and  $P_m$ , meaning that both the interference conditions as well as interface characteristics, namely the refractive index difference between core and cladding, change during the characterization process. Using the second fringe m = 2, located at 1591.22 nm for a temperature equal to 510°C (Figure 2), a good linear behavior is obtained with a sensitivity of 110 pm/°C for a large range of temperatures between 25 and 500°C. As for the intensity variation with temperature, a not so easily discernible trend is obtained, meaning this data is not adequate to be used for calibration purposes. Using the other two fringes, fringe 1, located at 1561.12 nm, and fringe 3, located at 1613.90 nm, linear behaviors are also achieved for the same range of temperatures with similar values for the sensitivities.

#### 2.2. Strain Measurements

The tested taper shows a considerable variation concerning the power  $P_2$  associated to the minima at 1541.30 nm, a value determined for no strain applied to the optical fiber. The wavelength of the resonant loss peak remains approximately the same throughout the entire characterization, with a sensitivity of 1 pm/ $\mu\epsilon$ , comprehending values of strain in the range between 0 to 1300  $\mu\epsilon$  (Figure 3). Such result can be explained by the fact that the applied strain does not change the interference conditions significantly. The refractive index difference  $\Delta n$  between core and cladding remains approximately unchanged as well as the difference in length  $\Delta L$  light travels along these two portions of the fiber, given the approximate mechanical stiffness. On the other hand, the applied strain might contribute to a reconfiguration of the transition region, making it even smoother which results in lower power losses, leading to an increase in the aver-



**Figure 3** Strain response of the interferometer taper using the first wavelength peak. Line (linear fit). [Color figure can be viewed in the online issue, which is available at wileyonlinelibrary.com]



**Figure 4** Curvature response of the interferometer taper using the first wavelength peak. Line (linear fit.). [Color figure can be viewed in the online issue, which is available at wileyonlinelibrary.com]

age transmitted power. The estimated variation in temperature resulting from applied strain is  $0.01^{\circ}\text{C}/\mu\epsilon$ . The maximum deviation from the average of the registered  $\lambda_2$  is 0.04%.

#### 2.3. Curvature Measurements

The calculation of the curvature can be performed in a rather smooth fashion. If the definition of curvature C is taken to be 1/R, then it can be explicitly written as a function of the easily measured distances L and h (vertical distance between the middle point of the curved fiber and the completely stretched fiber) [7]

$$R^2 = L^2 + (R - h)^2 \iff \frac{1}{R} = \frac{2h}{h^2 + L^2}$$
 (1)

Curvature measurements are performed and, once again, variations in both  $P_2$  and  $\lambda_2$  are registered. The changes in  $\lambda_2$  are not very significant in the curvature range between 0.14 and 0.70 m<sup>-1</sup> (Figure 4). The estimated variation in temperature resulting from applied curvature is  $4.55^{\circ}\text{C/m}^{-1}$ . The maximum deviation from the average of the registered  $\lambda_2$  is 0.02%.

#### 3. CONCLUSION

The proposed optical device is a good interferometer taper sensor of temperature, if one considers for calibration the curve of wavelength versus temperature, for the temperature range between 25 and 510°C with sensitivity 110 pm/°C. A remarkable result comes up when this taper is characterized in strain, since  $\lambda_2$  ends up being rather insensitive to changes in applied strain. The same conclusion can be withdrawn after testing the response of the taper in curvature, which renders this device a strain and curvature-independent temperature sensor. The estimated variations in temperature resulting from applied strain and curvature are  $0.001^{\circ}$ C/ $\mu\varepsilon$  and  $4.55^{\circ}$ C/m<sup>-1</sup>, respectively. To this outstanding characteristic one can add the fact that the temperature sensitivity is larger than the one verified for many tested devices in literature as well as the large range of temperatures one can cover resorting to this sensor, also benefiting from an easy and quick fabrication process.

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# THE EFFECT OF VOID ON CHARACTERISTICS OF LDMOS POWER AMPLIFIER

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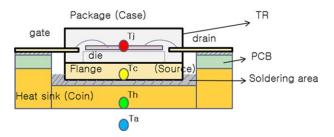
ABSTRACT: The effect of void on characteristics of the laterally diffused metal oxide semiconductor (LDMOS) power amplifier (PA) is analyzed using the thermal and circuit analysis together. Thermal analysis is performed for finding the junction temperature of LDMOS PA as void area changes. Circuit analysis is performed from these results and LDMOS PA libraries. The analysis results show the linearity, gain, and efficiency degradation in the LDMOS PA as a result of the increase in void area. And this simulation methodology can be used for the design of the PA. © 2016 Wiley Periodicals, Inc. Microwave Opt Technol Lett 58:691–694, 2016; View this article online at wileyonlinelibrary.com. DOI 10.1002/mop.29642

Key words: void; LDMOS; power amplifier; RRH; linearity

#### 1. INTRODUCTION

Modern wireless communication networks, such as wideband code division multiple access (WCDMA) and long-term evolution (LTE), make use of complex modulation schemes to maximize data throughput in a limited bandwidth. In general, their resultant communication signals exhibit a high peak-to-average power ratio (PAPR) [1]. Thus, high efficiency and linearity characteristics of power amplifiers (PAs) are required for these WCDMA and LTE base station (BS) applications.

Some microelectronic devices as well as high-power radio frequency (RF) PAs used in BSs typically dissipate non-negligible amounts of heat [2]. Thereby, the junction tempera-



**Figure 1** Cross-section view of LDMOS PA. (a) Tj: junction temperature, (b) Tc: case temperature, (c) Th: heat sink temperature, (d) Ta: ambient temperature. [Color figure can be viewed in the online issue, which is available at wileyonlinelibrary.com]

tures of PAs increase during operating conditions in the remote radio head (RRH) of BS [3].

Generally, a heat sink or coin has been used to pull heat from a PA to the outside. But the soldering parts between the heat sink and the PA have not been completely bonded in a practical manufacturing process, and hence a gap, or "void," is inevitable. The voids between a PA transistor and a heat sink can disturb the efficiency of the heat transformation [2].

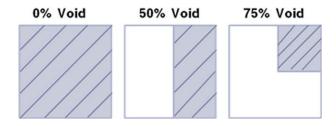
Previous research results have been reported for the junction temperature of a semiconductor device by using thermal analysis or a self-heating circuit model [4–8]. The relation between the void and the heat dissipation has been researched [9,10]. However, the previous researches of the void are focused on a view point of thermal parameter [11]. PA characteristic with void is rarely reported, especially for WCDMA or LTE BS application.

In this letter, the effect of void on characteristics of the laterally diffused metal oxide semiconductor (LDMOS) PA is analyzed using the thermal and circuit analysis together.

First, this paper presents modeling approaches as to how the thermal effect of the void area changes the junction temperature. Then, thermal analysis is performed on the LDMOS PA using heat-generation devices in RRH by varying the void area. With thermal analysis results, DC and RF characteristics of the LDMOS PA are analyzed for the circuit analysis in terms of linearity, gain, and efficiency of the PA for cases with 0%, 50%, and 75% void areas. Finally, both a WCDMA modulation signal and a CW signal are applied for simulations. Simulation analysis results are compared with the publically available measurement data, and conclusions are made. This is to verify that the simulation analysis is valid and can be used for the design of the PA.

### 2. STRUCTURE OF LDMOS POWER TRANSISTOR WITH VOID ON PRINTED CIRCUIT BOARD

Figure 1 shows a cross section of the LDMOS power transistor described as follows: gold wire bonds are used to make the connections between the lead and the die; gate and drain side-leads of the transistor are soldered to the printed circuit board (PCB);



**Figure 2** Top view of void area in soldering. [Color figure can be viewed in the online issue, which is available at wileyonlinelibrary.com]