

## CALL FOR APPLICATIONS: RESEARCHER

### Job/position/grant:

<b>Job reference:</b>	AE2020-0206 ( PEPCC - CTM ) INESC TEC - Instituto de Engenharia de Sistemas e Computadores, Tecnologia e Ciência
<b>Job/position/grant:</b>	
<b>City:</b>	Porto
<b>Research field:</b>	Main: ENGINEERING Sub: Electrical engineering

### Job summary:

<b>INESC TEC is accepting applications for 1 job in the Informatics Engineering area.</b>	
<b>Project:</b>	Power efficiency and performance for embedded and HPC systems with custom CGRAs
<b>Scientific Advisor:</b>	João Paiva Cardoso
<b>Duration of the contract:</b>	from 2021-01-01 to 2021-12-31
<b>Location:</b>	INESC TEC, Porto, Portugal

### Job description:

<b>Work Area:</b>	Informatics Engineering
<b>Project overview:</b>	- to contribute for the generation of efficient hardware accelerators implemented in FPGAs from streams of CPU instructions. - to contribute with techniques to improve the generation of hardware previously proposed by members of the project, namely with respect to dependence analysis, identification of memory regions without overlapping, and the techniques to extract data-streaming computations.
<b>Objectives:</b>	Research and development of techniques to generate automatically hardware accelerators implemented in FPGAs from streams of CPU instructions obtained from program executions.

<b>Academic Qualifications:</b>	Licenciado (BSc) and Master in Informatics Engineering or in related areas
<b>Minimum profile required:</b>	Average score of the Licenciatura (BSc) and Master greater than 14 points
<b>Preference factors:</b>	- Experience in implementing techniques for code analysis, in compiler engineering tasks, knowledge and use of high-level synthesis tools, and FPGAs and hardware accelerators. - Master's Dissertation in the area of compilers and mainly in code optimizations for high-level synthesis for FPGAs;

<b>Funding Entity:</b>	Financed with national funds by FCT - Portuguese Foundation for Science and Technology, I.P., in the scope of the project PTDC/EEI-HAC/30848/2017
<b>Type of contract:</b>	Certain Term
The hiring shall be governed by what is stipulated in the legislation in force regarding fixed individual employment contracts and by INESC TEC norms.	

<b>Selection criteria:</b>	The selection of the candidates will be based on the following criteria, in descending order of consideration: a) Relevant Curriculum in the concerned field of this tender b) Proven experience.
<b>Selection Jury:</b>	President of the Jury: Prof. João Canas Ferreira; Member: Prof. João Paiva Cardoso; Member: Prof. José Carlos Alves;
<b>Notification of results:</b>	The results of the selection process will be sent to the interested by electronic mail.
<b>Application period:</b>	From 2020-11-16 to 2020-11-30
<b>Application submission:</b>	Electronic form filling in <a href="http://www.inesctec.pt">www.inesctec.pt</a> in the section <a href="#">Work with Us</a>